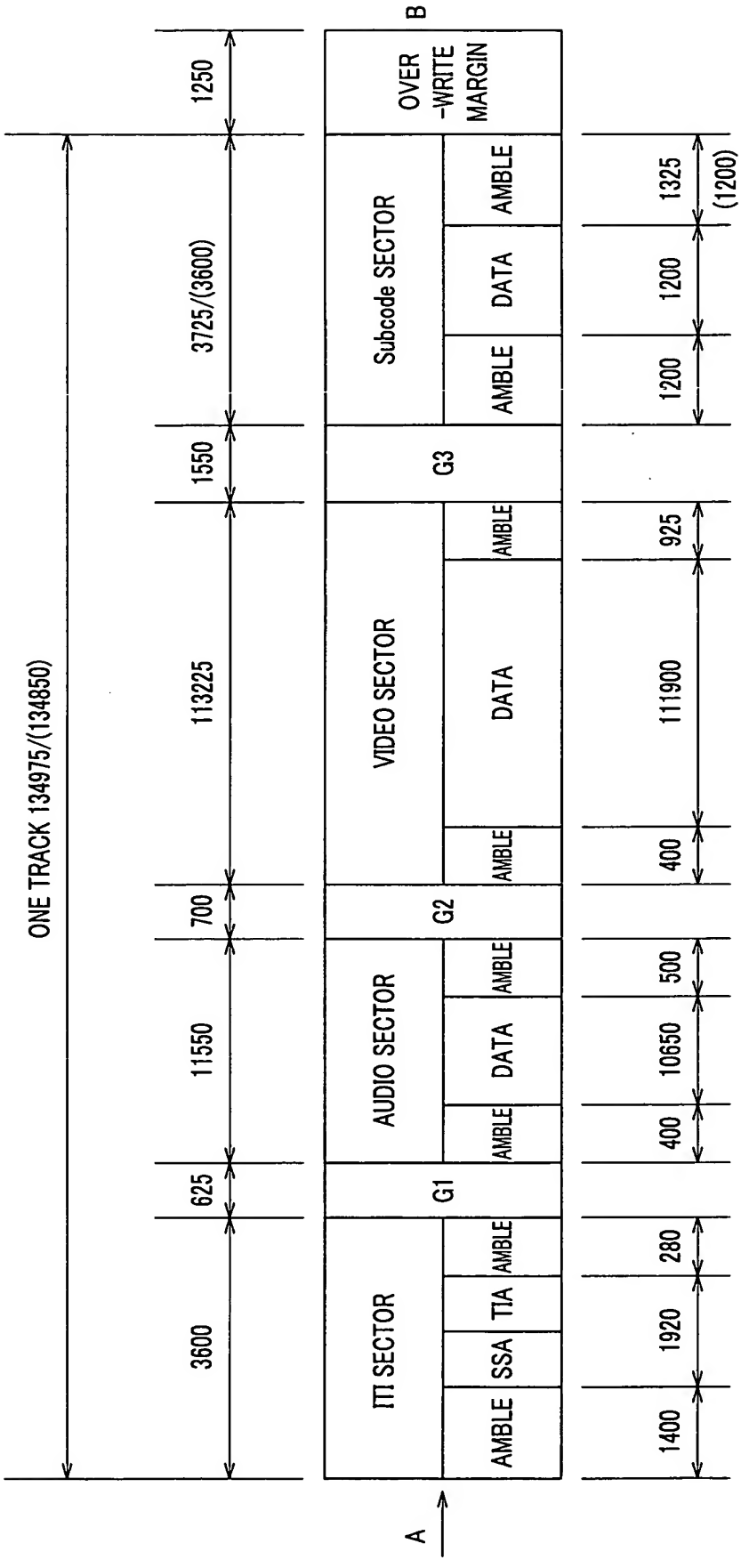
ONE TRACK 134975/(134850)
3600
625
11550
700
113225
1550
3725/(3600)
1250
ITI SECTOR
AMBLE
SSA
TIA
AMBLE
G1
AUDIO SECTOR
AMBLE
DATA
AMBLE
G2
VIDEO SECTOR
AMBLE
DATA
AMBLE
G3
Subcode SECTOR
AMBLE
DATA
AMBLE
OVER -WRITE MARGIN
A
B
1400
1920
280
400
10650
500
400
111900
925
400
1200
1200
1325
(1200)

FIG. 1

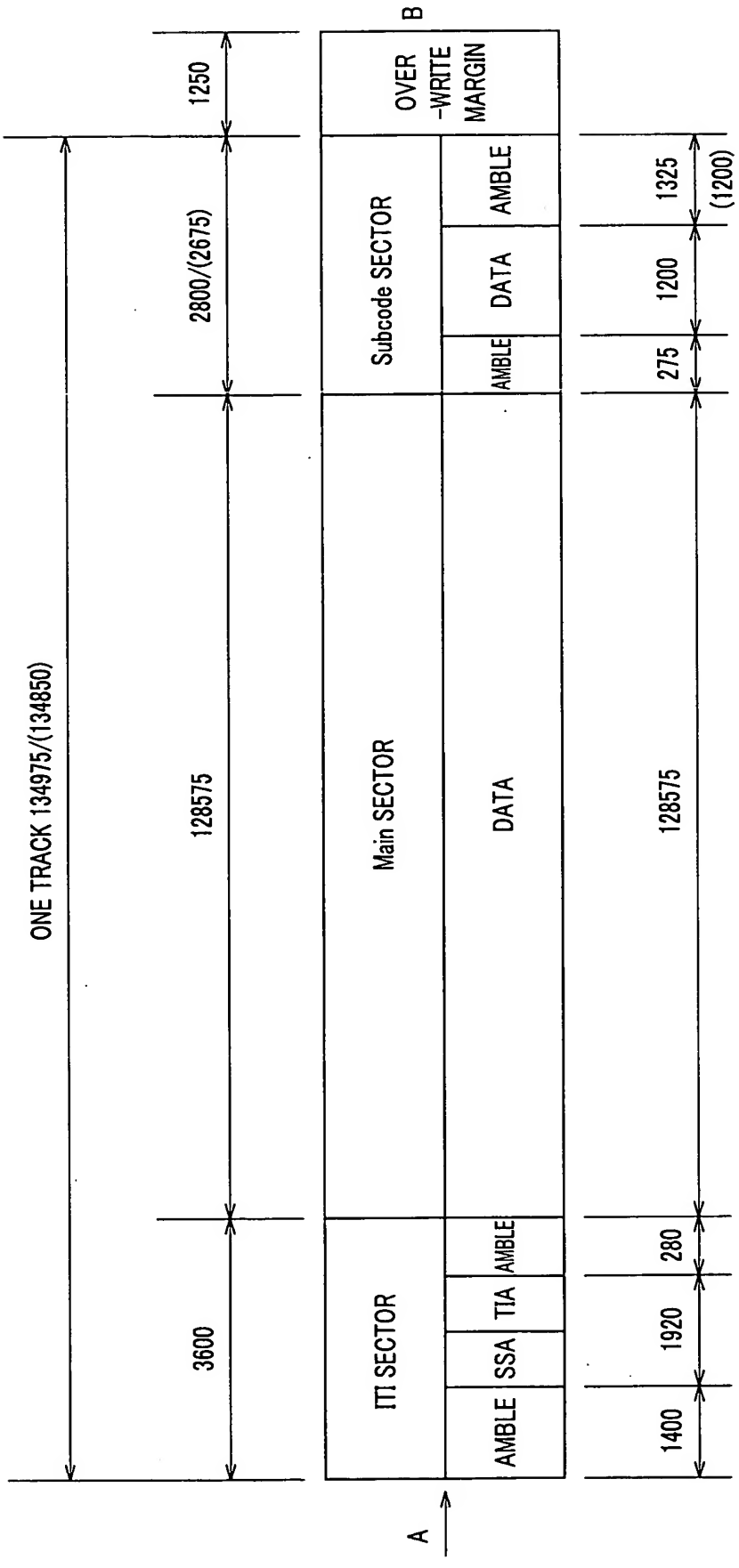
ONE TRACK 134975/(134850)
1250
2800/(2675)
128575
3600
OVER -WRITE MARGIN
Subcode SECTOR
AMBLE
DATA
AMBLE
Main SECTOR
DATA
ITI SECTOR
AMBLE
SSA
TIA
AMBLE
1325
(1200)
1200
275
128575
280
1920
1400
A
B

FIG. 2

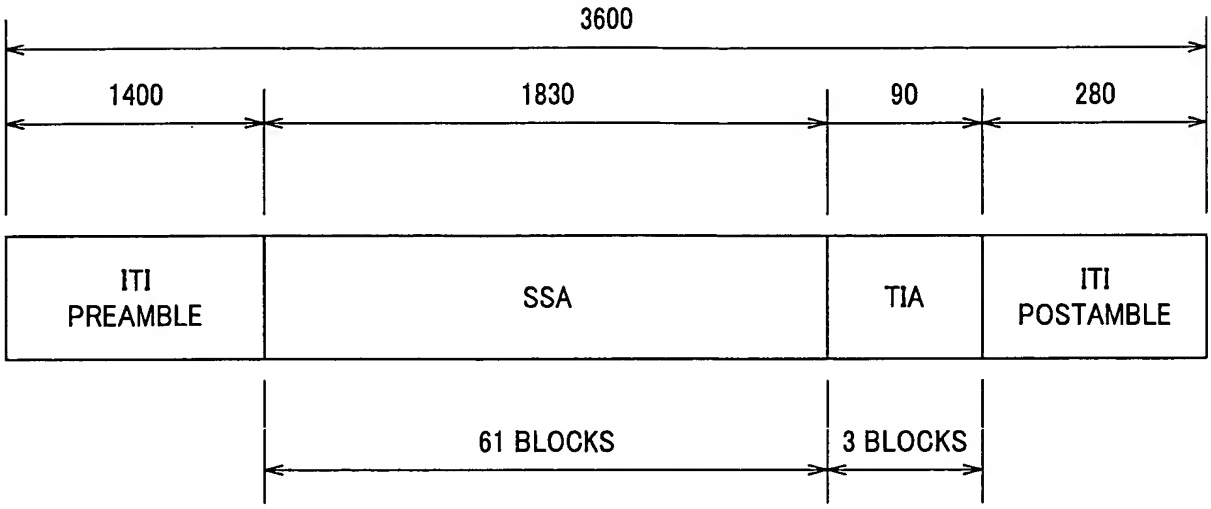

FIG. 3

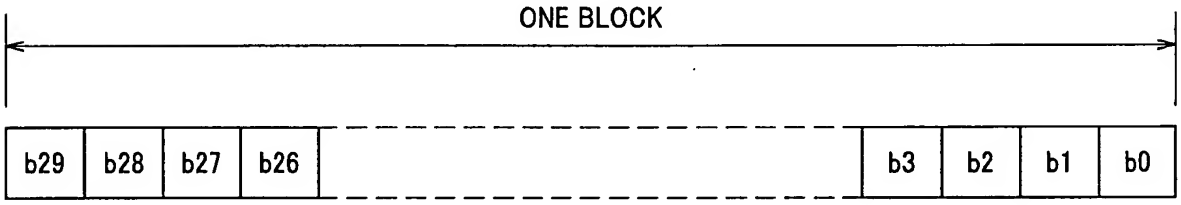

FIG. 4

b12	b13	b14	b15	b16	b17	b22	b23	b24	b25	b26	b27
APT_2	APT_2	APT_1	APT_1	APT_0	APT_0	TP_1	TP_1	TP_0	TP_0	PF	PF
0	0	1	1	0	0	1	1	1	1	0	0

FIG. 5

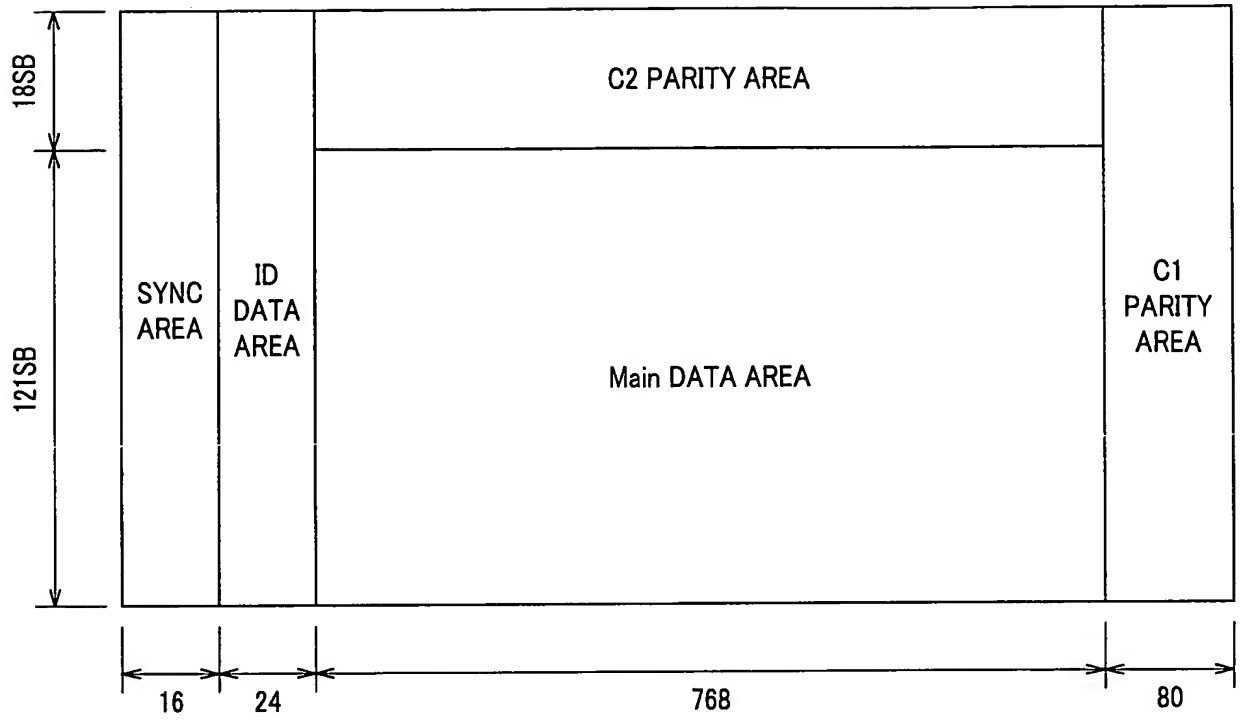
18SB
121SB
SYNC AREA
ID DATA AREA
C2 PARITY AREA
Main DATA AREA
C1 PARITY AREA
16
24
768
80

FIG.6

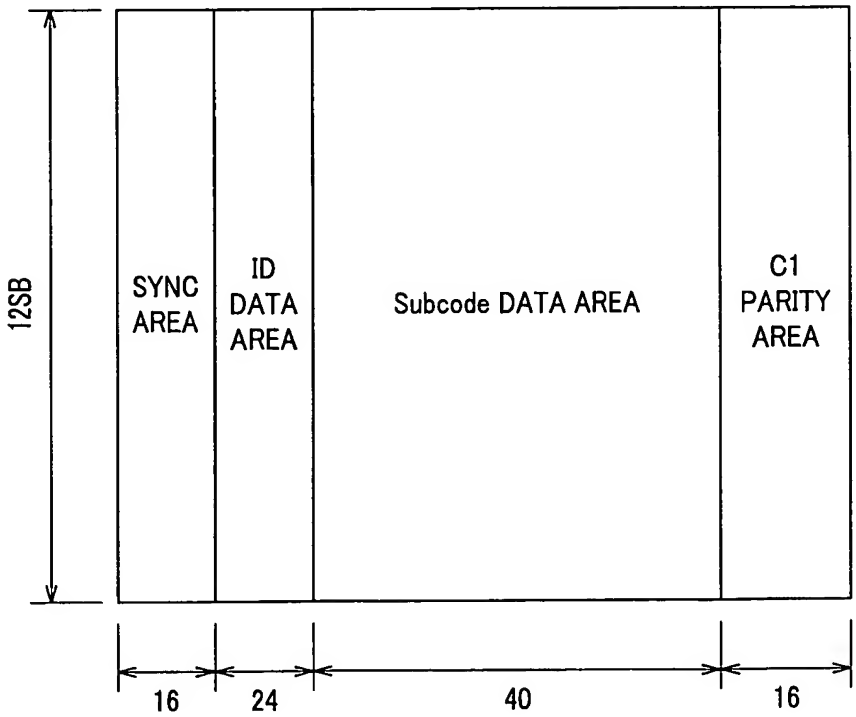
12SB
SYNC AREA
ID DATA AREA
Subcode DATA AREA
C1 PARITY AREA
16
24
40
16

FIG.7

(A)

	ONE Frame (10track)									
Track No.	0	1	2	3	4	5	6	7	8	9
Subcode Data	First Track data					Second Track data				

(B)

	ONE Frame (12track)											
Track No.	0	1	2	3	4	5	6	7	8	9	10	11
Subcode Data	First Track data						Second Track data					

FIG. 8

First Track Data					
SB No.	DATA area Byte Position Number				
	D0	D1	D2	D3	D4
0	ETN				
1	ETN or TGB				
2	ETN				
3	TTC				
4	TTC or TBG or NOI				
5	TTC				
6	ETN				
7	ETN or TGB				
8	ETN				
9	TTC				
10	TTC or TBG or NOI				
11	TTC				

FIG. 9A

Second Track Data					
SB No.	DATA area Byte Position Number				
	D0	D1	D2	D3	D4
0	ETN				
1	ETN				
2	ETN				
3	TTC				
4	VRD or ARD or NOI				
5	VRD or ARD or NOI				
6	ETN				
7	ETN				
8	ETN				
9	TTC				
10	VRD or ARD or NOI				
11	VRD or ARD or NOI				

FIG. 9B

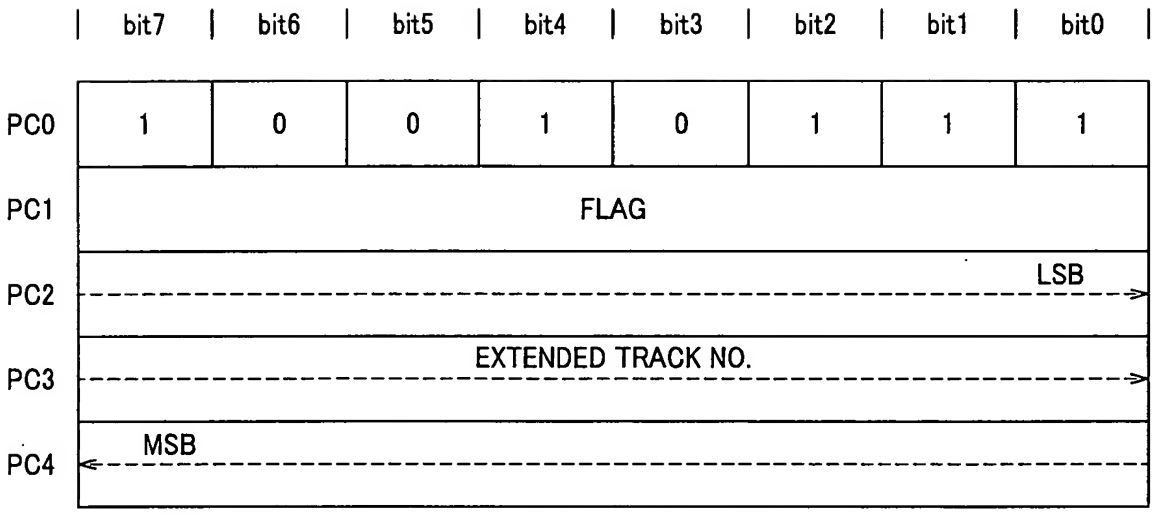
bit7
bit6
bit5
bit4
bit3
bit2
bit1
bit0
PC0
1
0
0
1
0
1
1
1
PC1
FLAG
PC2
LSB
PC3
EXTENDED TRACK NO.
PC4
MSB

FIG.10

ID0								ID1							
MSB							LSB	MSB							LSB
b7	b6	b5	b4	b3	b2	b1	b0	b7	b6	b5	b4	b3	b2	b1	b0

FR	$AP3_2$	$AP3_1$	$AP3_0$	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0
FR	$AP3_2$	$AP3_1$	$AP3_0$	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	APT_2	APT_1	APT_0	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0

FIG. 11

ID0								ID1							
MSB							LSB	MSB							LSB
b7	b6	b5	b4	b3	b2	b1	b0	b7	b6	b5	b4	b3	b2	b1	b0

FR	$AP3_2$	$AP3_1$	$AP3_0$	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0
FR	$AP3_2$	$AP3_1$	$AP3_0$	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST	LSB	BF	Syb_3	Syb_2	Syb_1	Syb_0
FR	Index	Skip	PP	ABST			Syb_3	Syb_2	Syb_1	Syb_0
FR	0	0	0	ABST MSB			Syb_3	Syb_2	Syb_1	Syb_0

FIG. 12

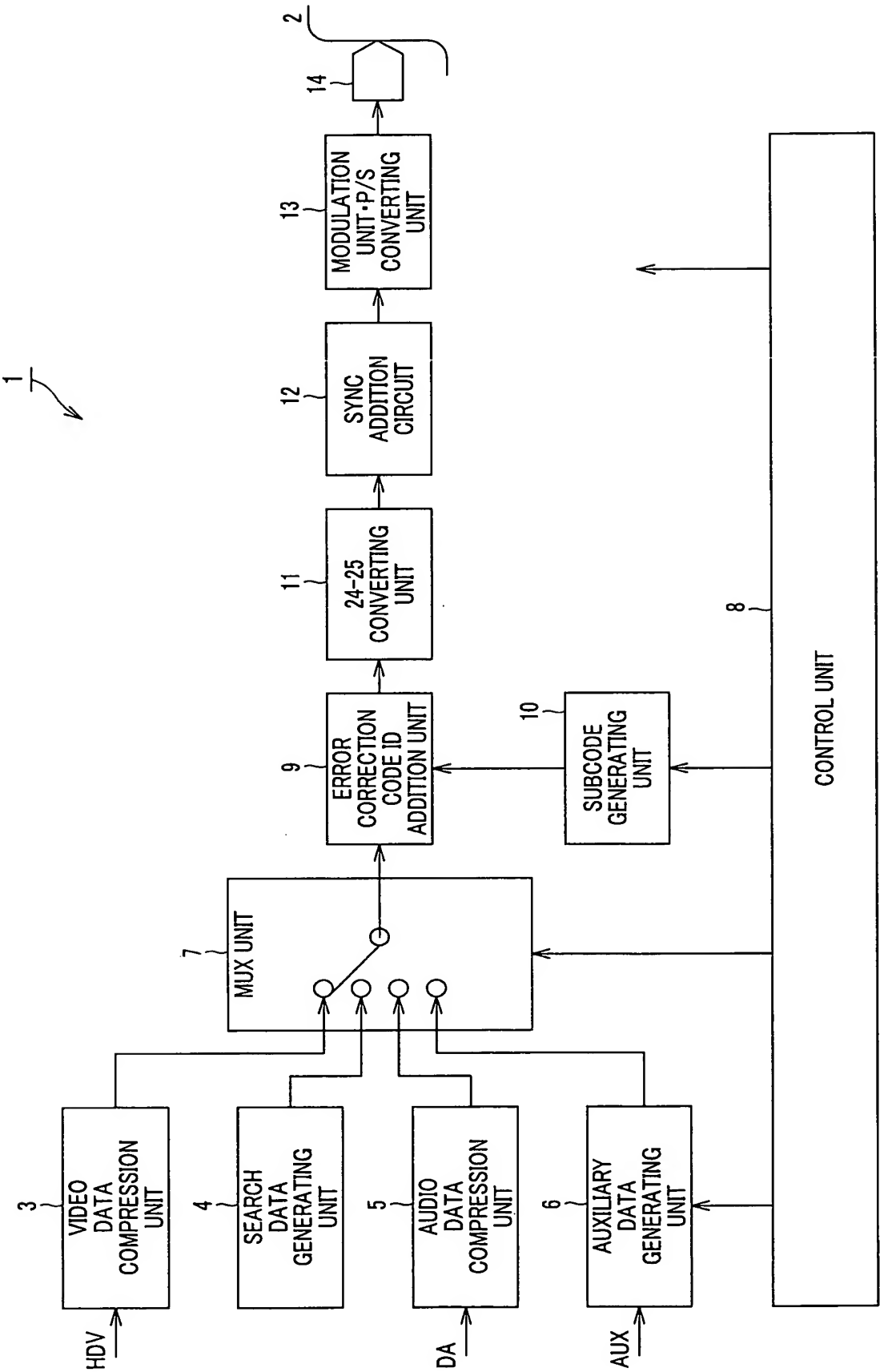
1
2
3
VIDEO DATA COMPRESSION UNIT
4
SEARCH DATA GENERATING UNIT
5
AUDIO DATA COMPRESSION UNIT
6
AUXILIARY DATA GENERATING UNIT
7
MUX UNIT
8
CONTROL UNIT
9
ERROR CORRECTION CODE ID ADDITION UNIT
10
SUBCODE GENERATING UNIT
11
24-25 CONVERTING UNIT
12
SYNC ADDITION CIRCUIT
13
MODULATION UNIT•P/S CONVERTING UNIT
14
HDV
DA
AUX

FIG.13

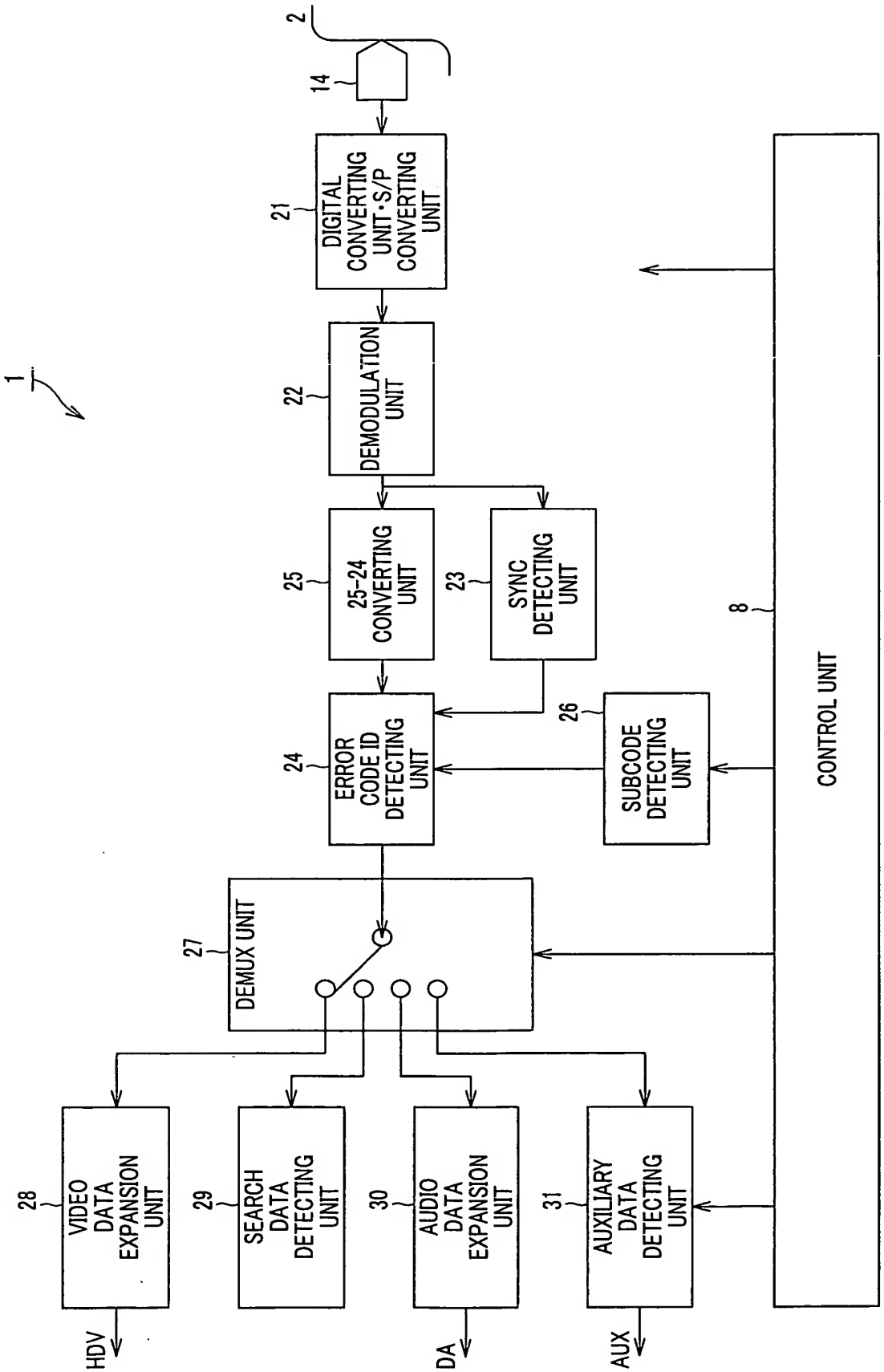
1
2
14
21
DIGITAL CONVERTING UNIT·S/P CONVERTING UNIT
22
DEMODULATION UNIT
25
25-24 CONVERTING UNIT
23
SYNC DETECTING UNIT
8
CONTROL UNIT
24
ERROR CODE ID DETECTING UNIT
26
SUBCODE DETECTING UNIT
27
DEMUX UNIT
28
VIDEO DATA EXPANSION UNIT
29
SEARCH DATA DETECTING UNIT
30
AUDIO DATA EXPANSION UNIT
31
AUXILIARY DATA DETECTING UNIT
HDV
DA
AUX

FIG.14